

FIG. 1

Process order	0	1	2	3	4	5	6	7
Input frame	I0	P1	B2	B3	P4	B5	B6	P7
Frame to be subjected to forward motion detection		P1	B2	B3	P4	B5	B6	P7
Forward motion detection reference frame		I0	P1	P1	P1	P4	P4	P4
Frame to be subjected to backward motion detection					B3	B2		
Backward motion detection reference frame					P4	P4		
Forward motion compensated frame				I0	P1	P1	P1	P4
Backward motion compensated frame						P4	P4	
Frame to be subjected to DCT / Q / IQ / IDCT / VLC			I0	P1	P4	B2	B3	P7

FIG. 2

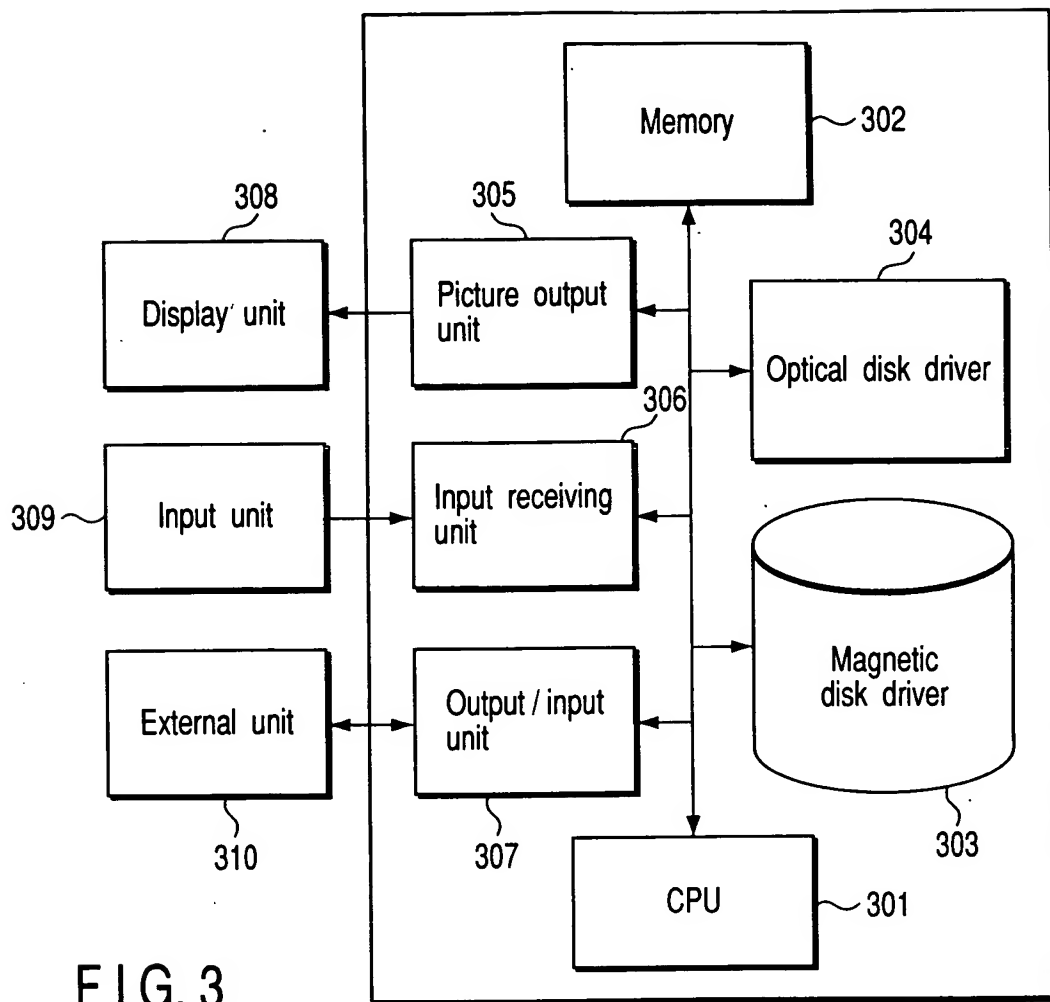


FIG. 3

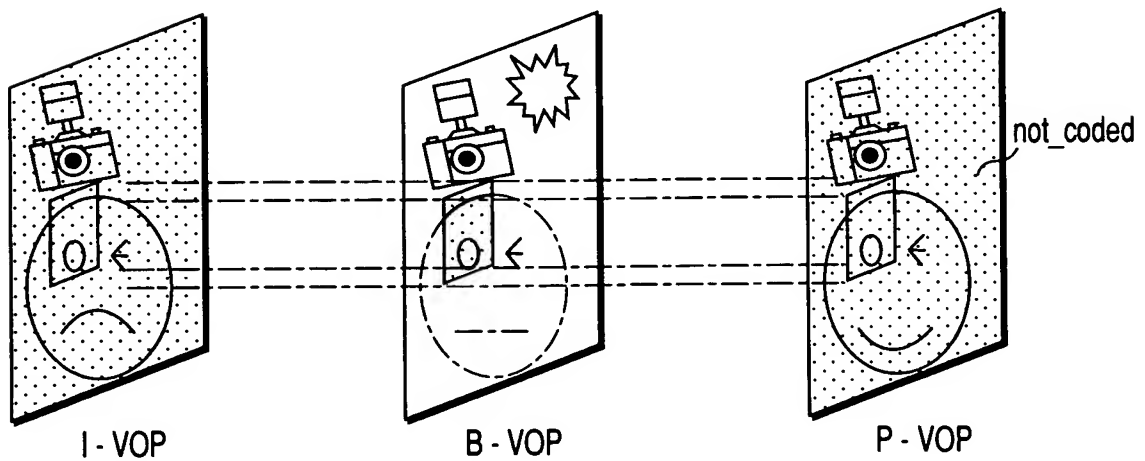


FIG. 7

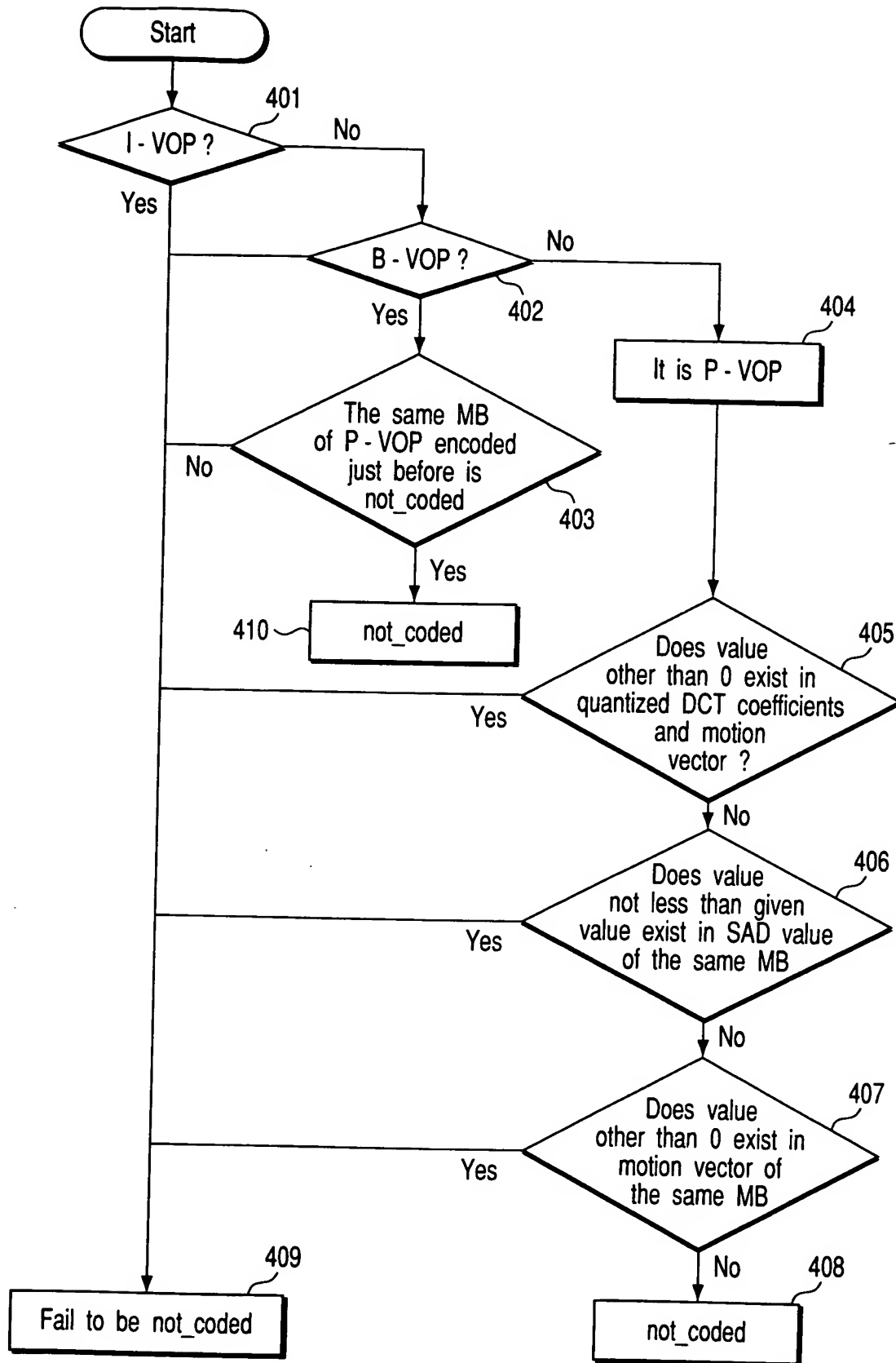


FIG. 4

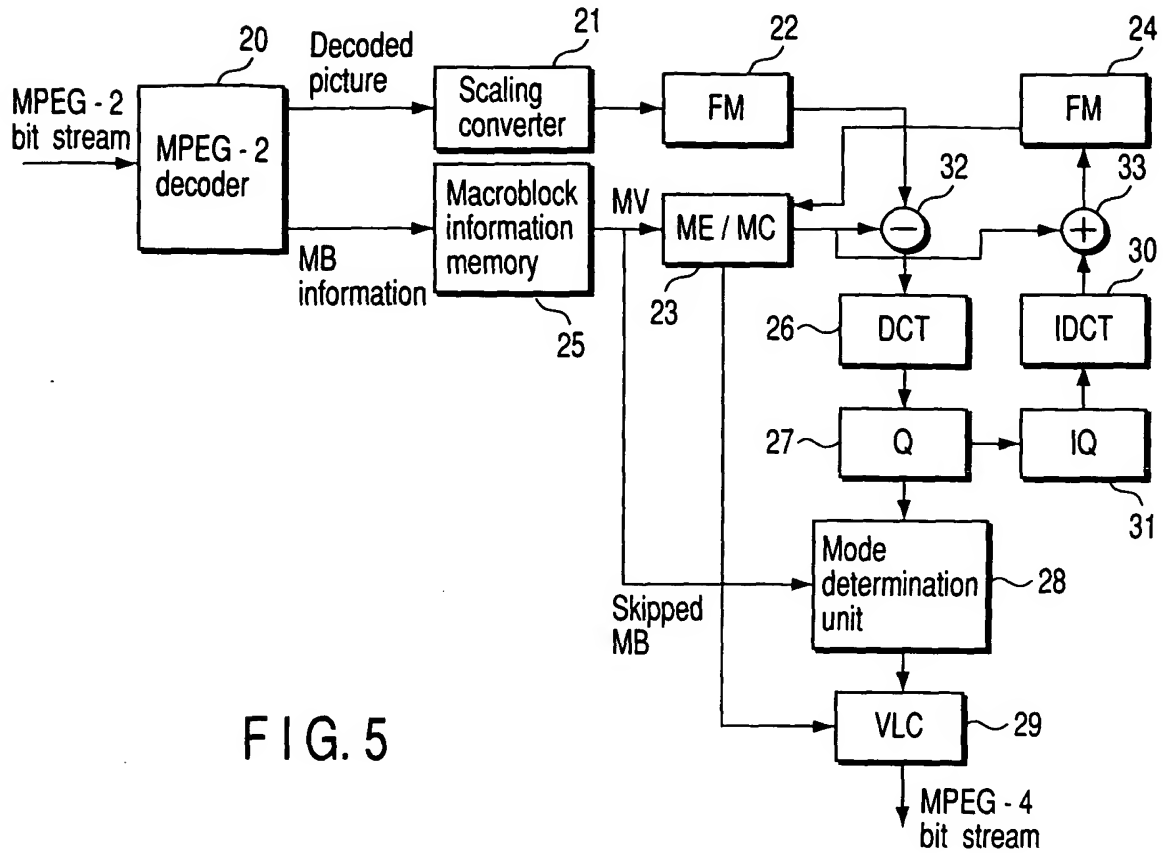


FIG. 5

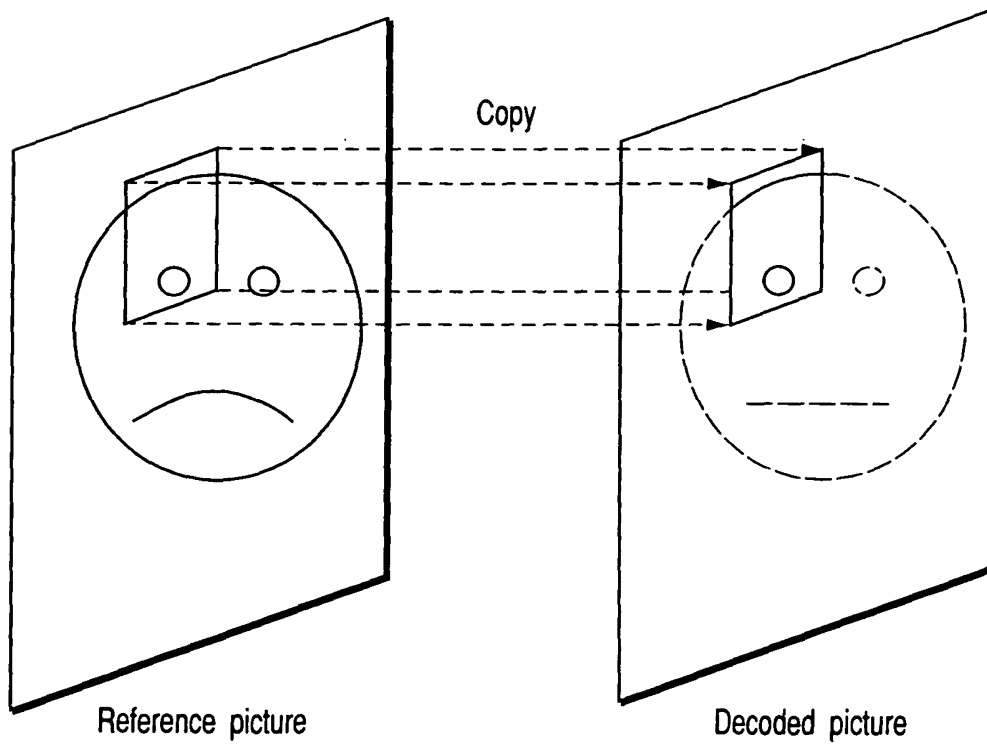


FIG. 6

Process order	0	1	2	3	4	5	6	7
Input frame	I0	P1	B2	B3	P4	B5	B6	P7
Frame to be subjected to forward motion detection				P1	P4	B2	B3	P7
Forward motion detected reference frame / forward motion compensated frame				I0	P1	P1	P1	P4
Frame to be subjected to backward motion detection						B2	B3	
Backward motion detected reference frame / backward motion compensated frame						P4	P4	
Frame to be subjected to DCT / Q / IQ / IDCT / VLC			I0	P1	P4	B2	B3	P7

FIG. 8

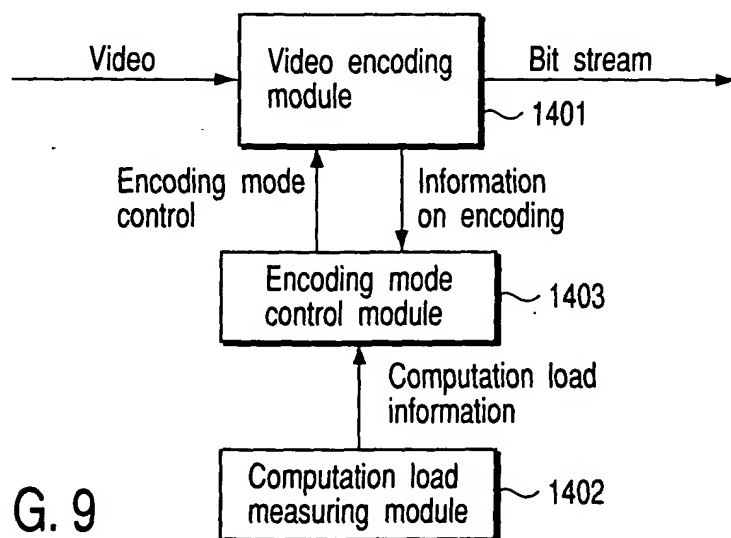


FIG. 9



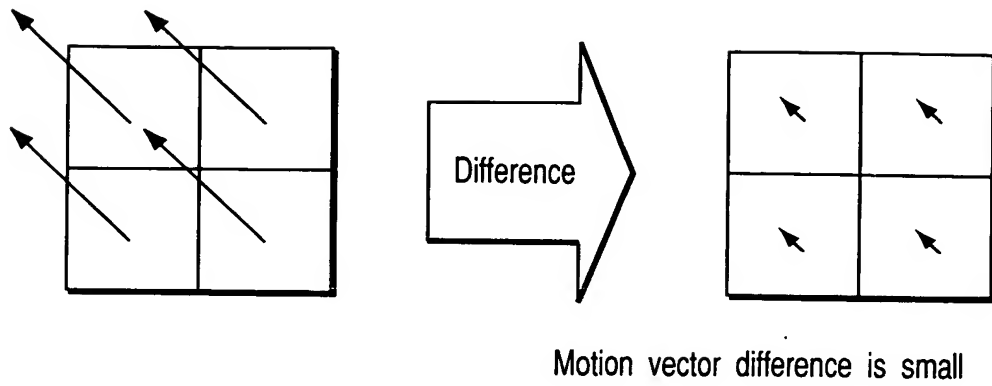


FIG. 11A

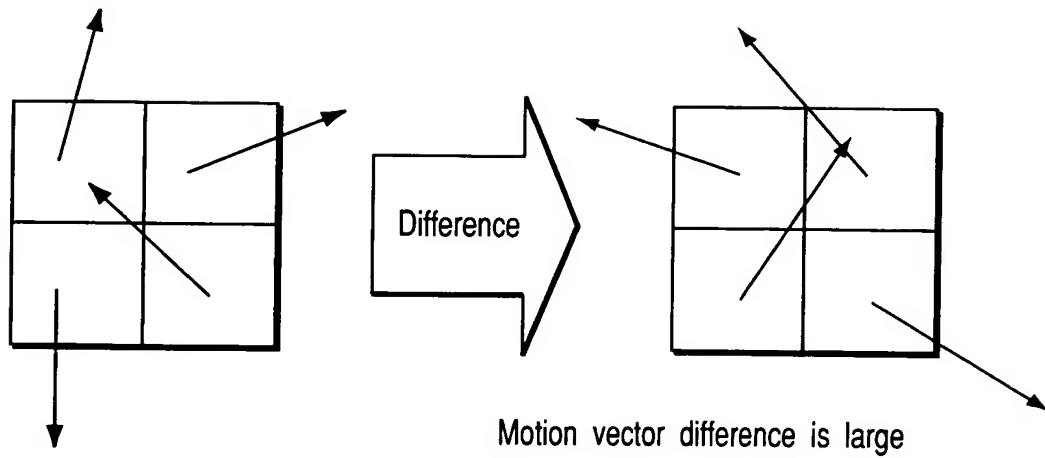


FIG. 11B

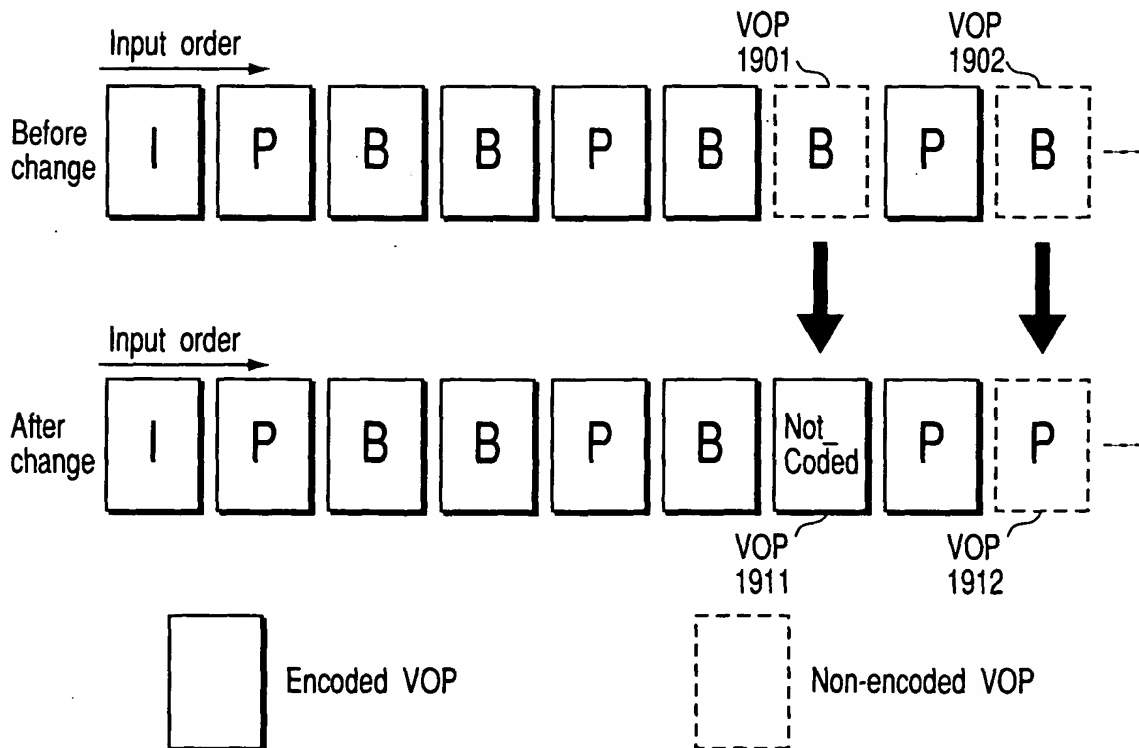
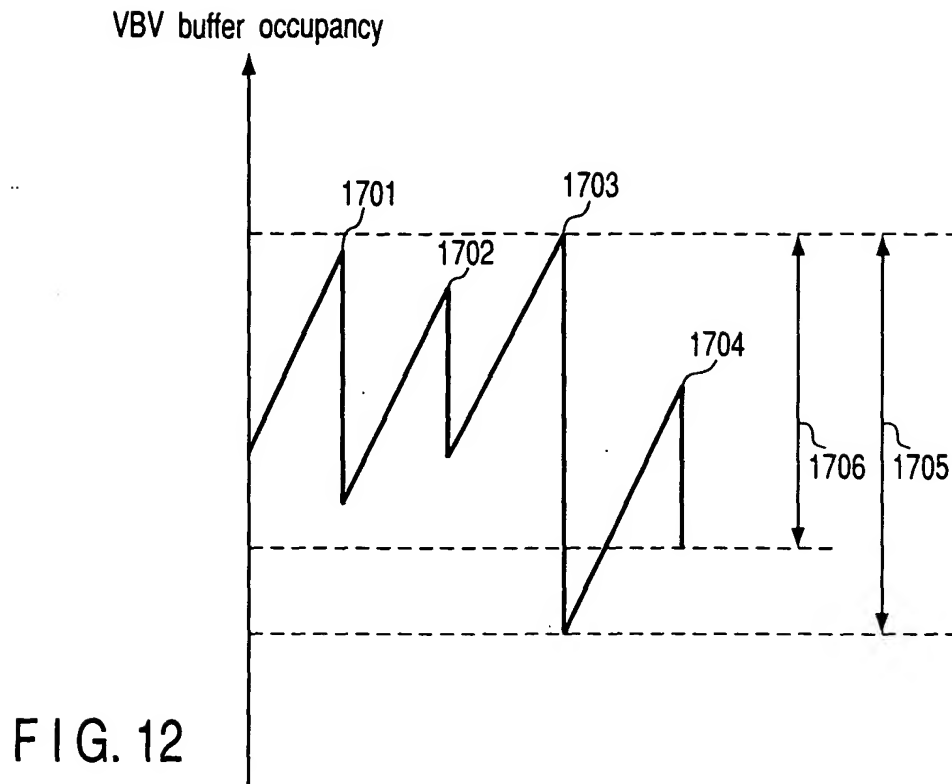


FIG. 14



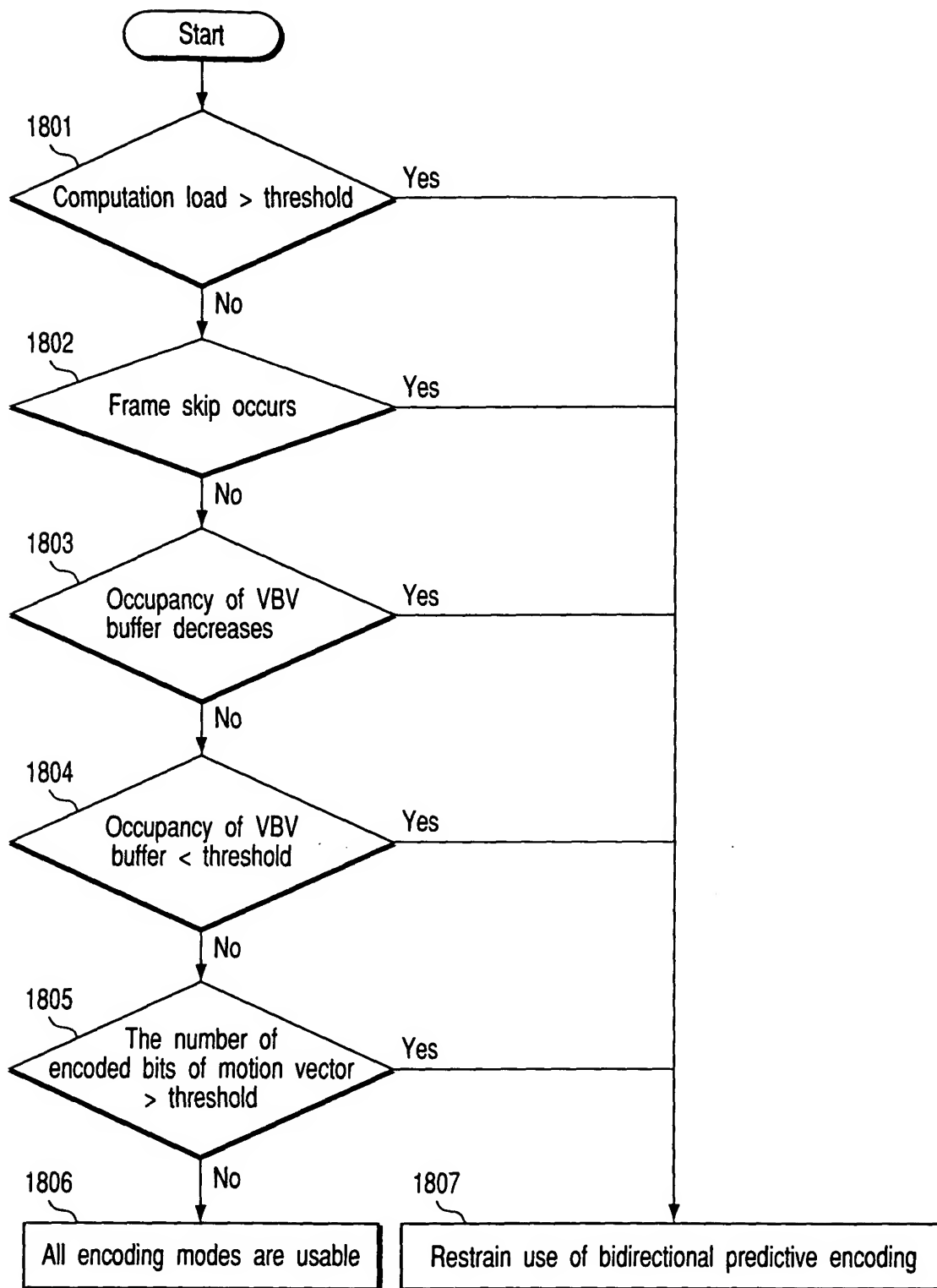


FIG. 13

